BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT

Avalahalli Yelahanka Banglore – 560064

(AN AUTONOMOUS INSTITUTE, AFFILIATED TO VTU, BELAGAVI; APPROVED BY AICTE, NEW DELHI; 7 PROGRAMS ACCREDITED BY NBA; NAAC ACCREDITED WITH 'A' GRADE)

Department of Electronics and Telecommunication

OPEN COURSE/SKILL DEVELOPMENT COURSE
ON
"PCB & SYSTEM HARDWARE DESIGN"

TRAINING BY INDUSTRY EXPERTS
&
VISIT TO MANUFACTURING INDUSTRY

DATE: 12TH JUNE – 16TH JUNE

Course contents:
1. Introduction to CADENCE ORCAD
2. Working on Schematics(Hands–on)
3. Hardware design life cycle
4. Working on PCB layouts
5. Fabrication Cycle

Course outcomes:
Students will be able to
1. Design schematic & Layout of PCB
2. Design single and multiple layer PCB
3. Design and fabrication process of PCB with Industrial standards
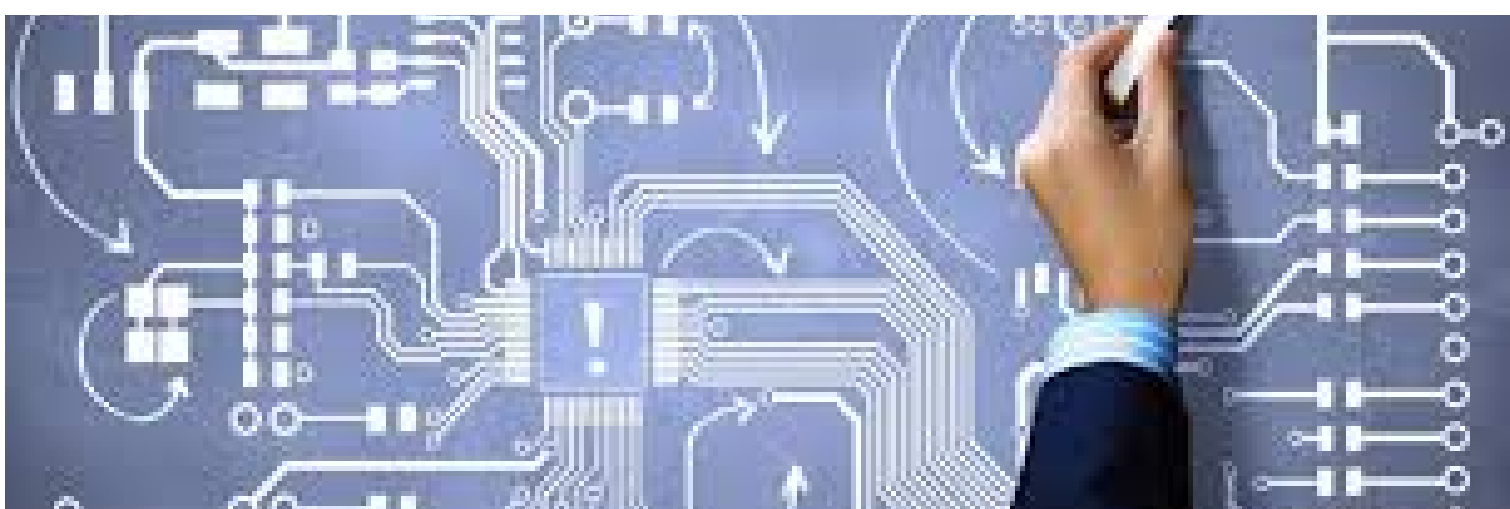4. Evaluate and testing of PCB design using industrial protocols

Resource person:
Mr. Karunakaran S
SCHEMAZEN TECHNOLOGIES

!! OPEN TO STUDENTS FROM ALL STREAMS OF ENGINEERING AND SCIENCE !!

Registeration fee Rs.400
Payment mode : Online
How to pay-
1. Login into https://bmsitm.gnums.in
2. Go to menu and select other fee
3. Select fee head as open course fee and pay 400/–

COORDINATORS:
DR RAJU HAJARE
ASSOCIATE PROF. ETE DEPT. BMSIT&M

TECHNICAL SUPPORT
MR H N PRAKASH KUMAR
FOREMAN

Department of Electronics & Telecommunication Engineering
Course Schedule: "PCB and System Hardware Design"
12 th June 2023 to 16 th June 2023

Sl.no	Date	Topics Covered 8:30 – 10:30am	Topics Covered 10:50 – 12:50pm	Topics Covered 2:00 – 4:00pm / (4:00pm-4:30pm – Feedback)
1	12.06.2023	**Inauguration**, Introduction to Electronics industry, Products Role of product designers, Bridging the Gap between Industry and Academics	Introduction to system Design life cycle, Hardware design life cycle various types of processors, ICS used for product design component packages	Introduction to tools CADENCE ORCAD for designing the products, introduction to cadence allegro for designing products [with HANDS-ON]
2	13.06.2023	Electronics Standard Interfaces used in system design [I2C, SPI, UART]	Working on Schematics for the product [with HANDS-ON]	Working on PCB Layout for the Product [with HANDS-ON]
3	14.06.2023	Product Manufacturing	Working on Schematics for the product [with HANDS-ON]	Component selection Engineering, Working on PCB Layout for the Product [with HANDS-ON]
4	15.06.2023	Visit to PCB Manufacturing Unit	Visit to PCB Manufacturing Unit	Visit to PCB Manufacturing Unit
5	16.06.2023	PCB Production, Quality check	CB Functional test, load test, dispatc	Valedictory

Tea break (10:30 - 10:50 am)

Lunch break (12:50 - 1:50 pm)

Instructions to th participants:
Students are requested to carry their laptops